

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	4	(shift adj register and cascade and controller and buffer and transistor).clm.	US-PGPUB	OR	ON	2007/02/13 09:12
L2	9	(shift adj register and cascade and stage and buffer and transistor).clm.	US-PGPUB	OR	ON	2007/02/13 09:12